

Fig. 1

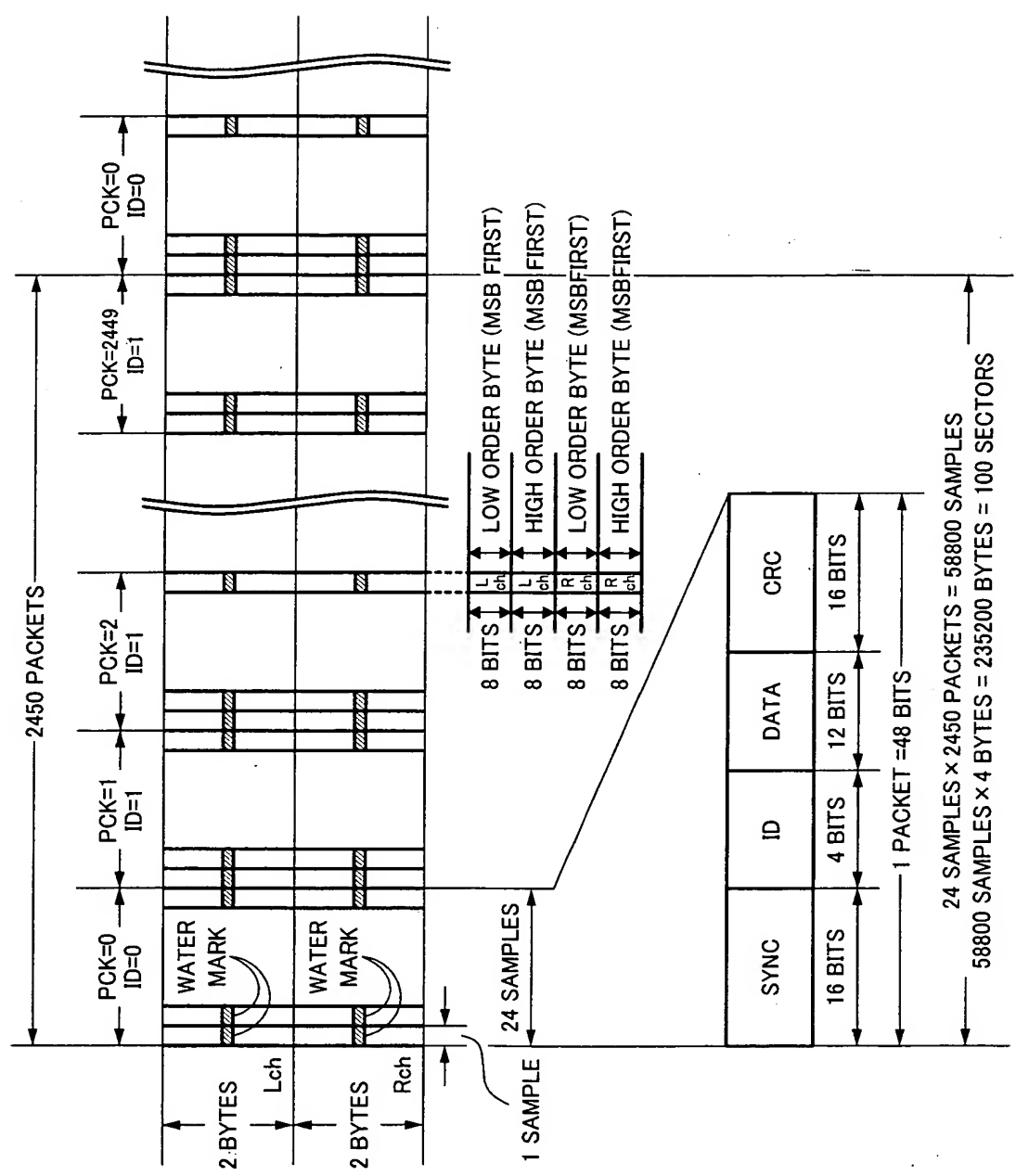
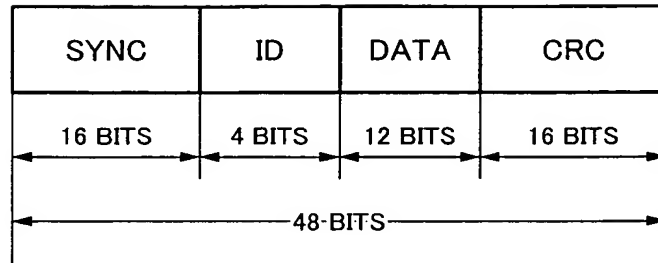
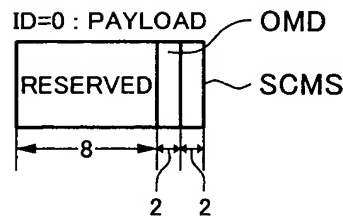


Fig. 2A



SYNC:5555(h)

Fig. 2B



OMD

0 0 : ROM (PRE-PRESS)

0 1 : R

1 0 : RW

1 1 : ANY(ROM/R/RW)

SCMS

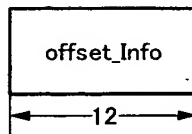
0 0 : ORIGINAL

0 1 : COPY

1 1 : COPY FREE

Fig. 2C

ID=1 : PACKET ODDSET



offset_Info = 2450 - PACKET NUMBER

Fig. 3

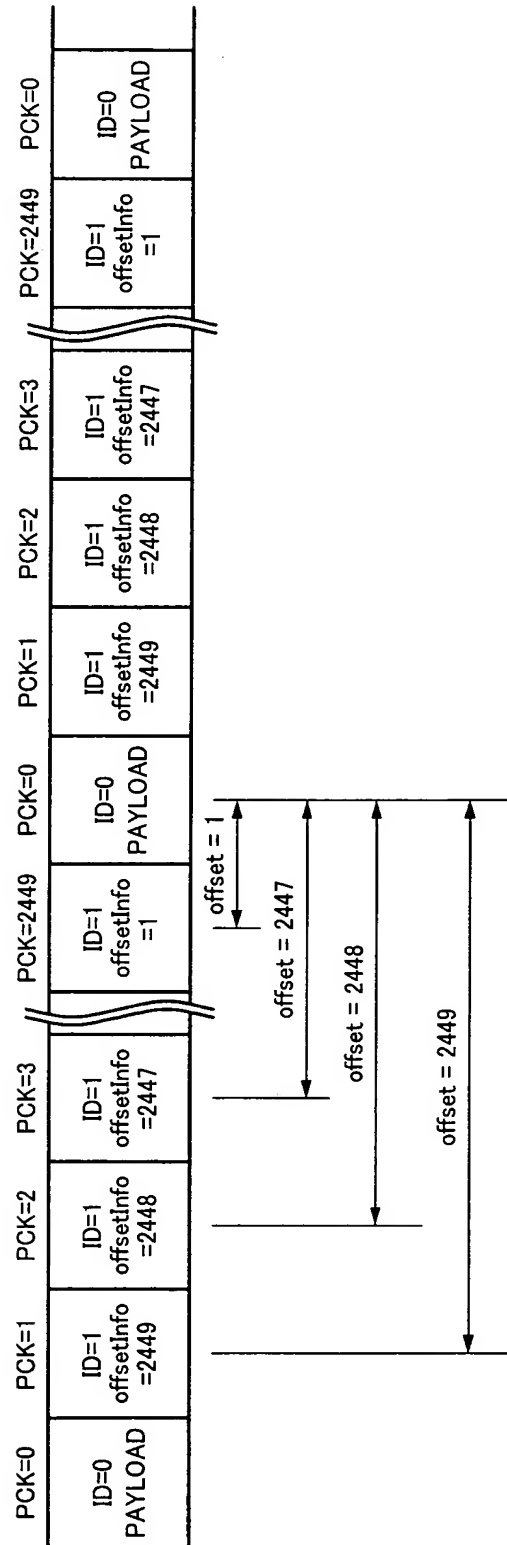


Fig. 4

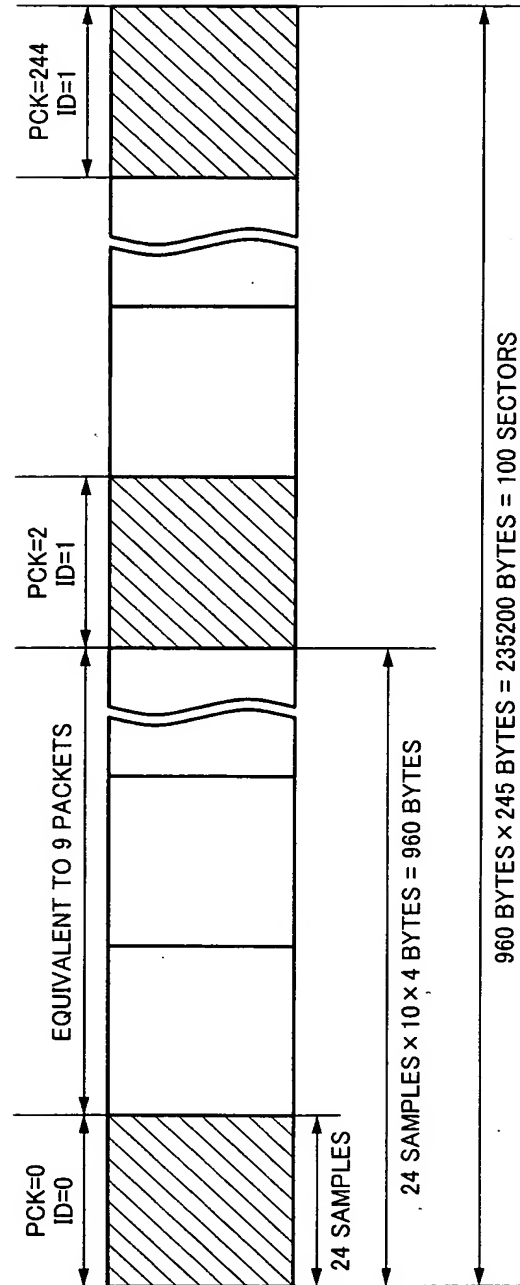


Fig. 5

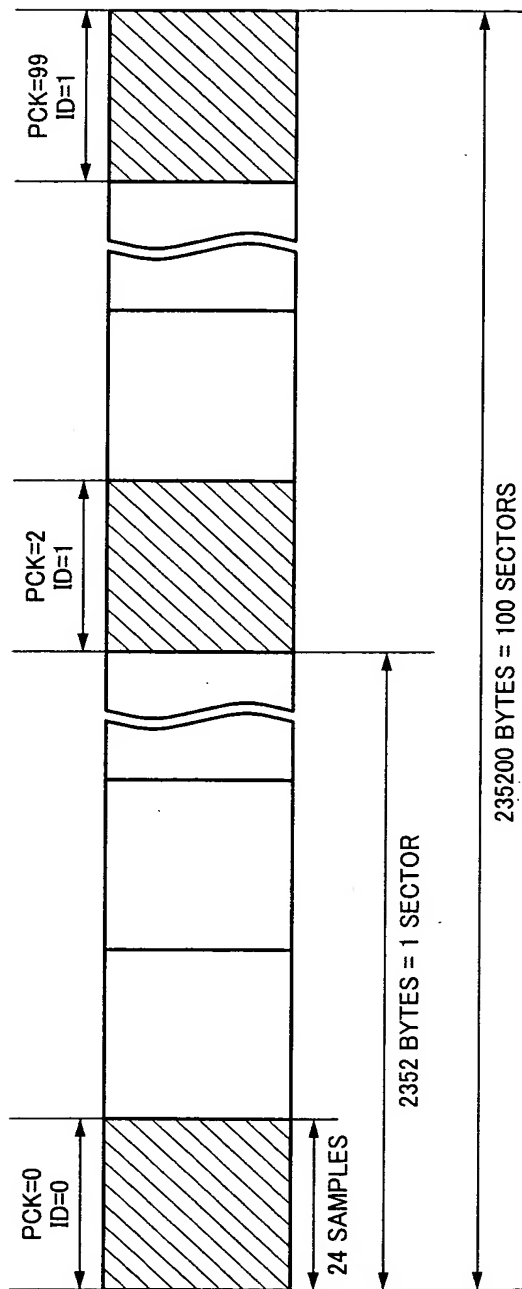


Fig. 6A

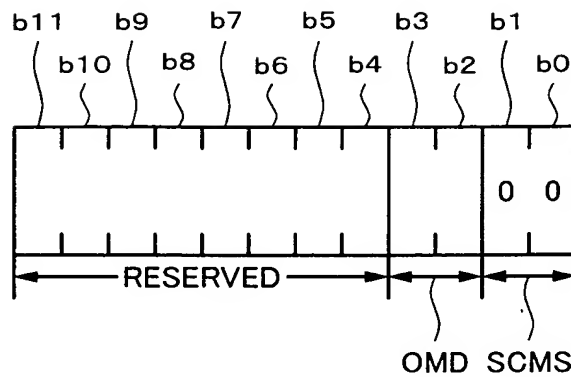


Fig. 6B

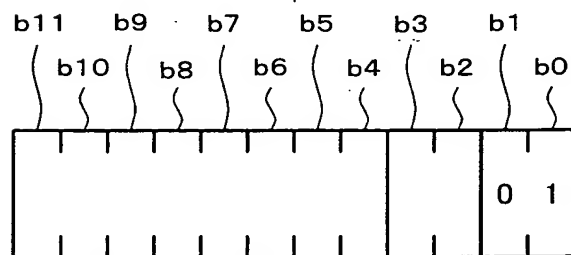


Fig. 7

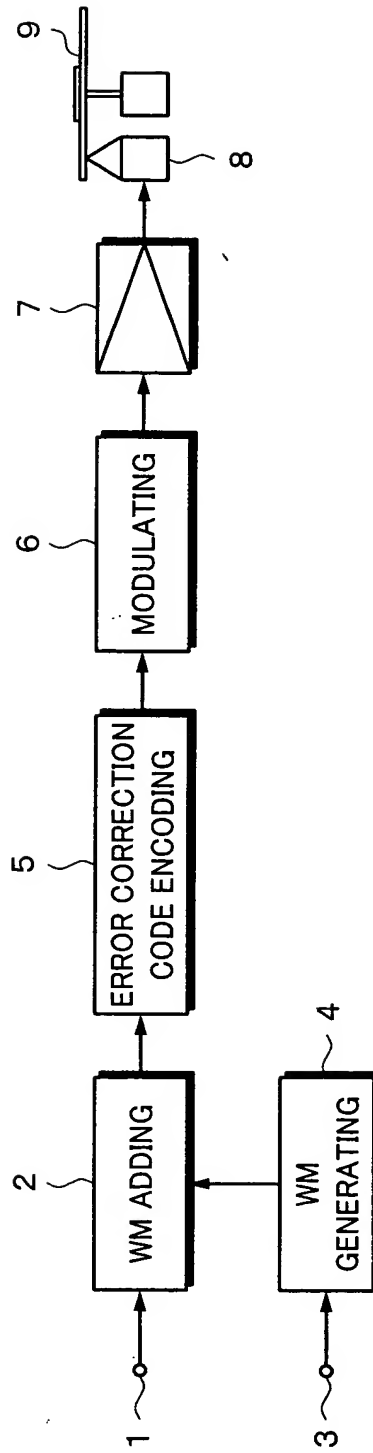


Fig. 8

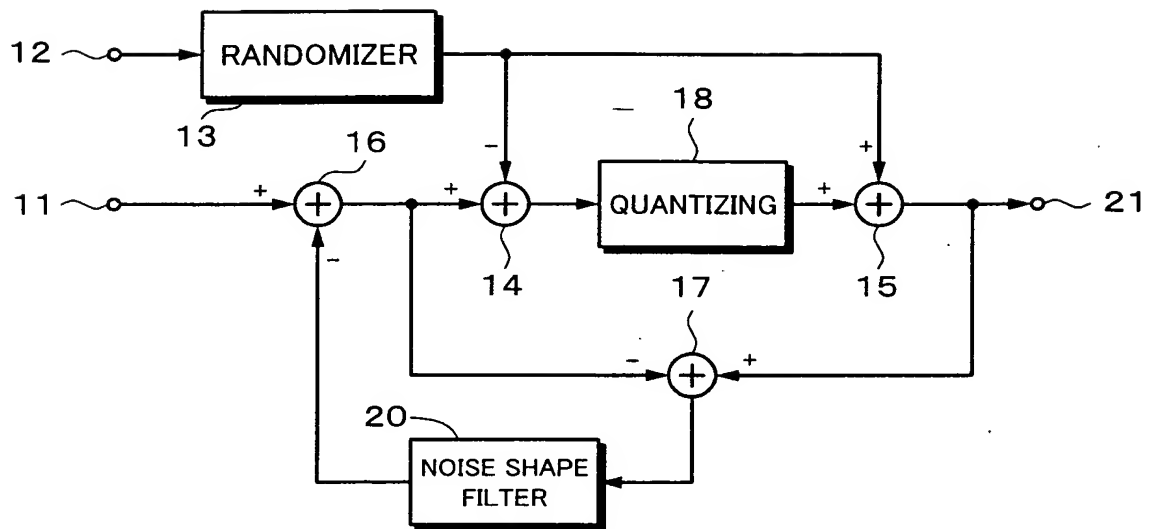


Fig. 9

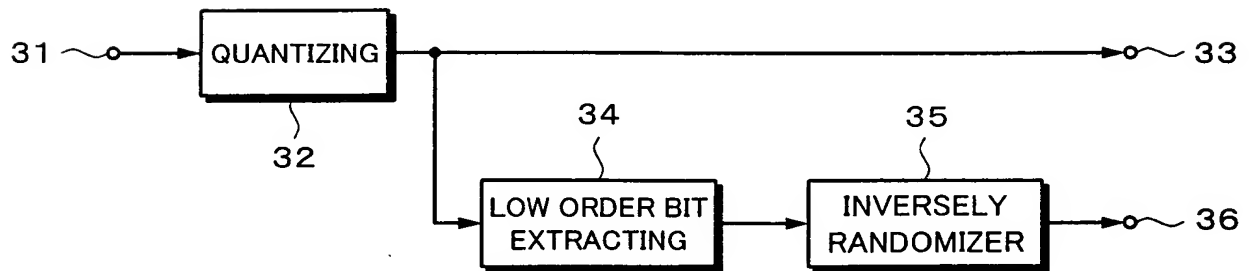


Fig. 10

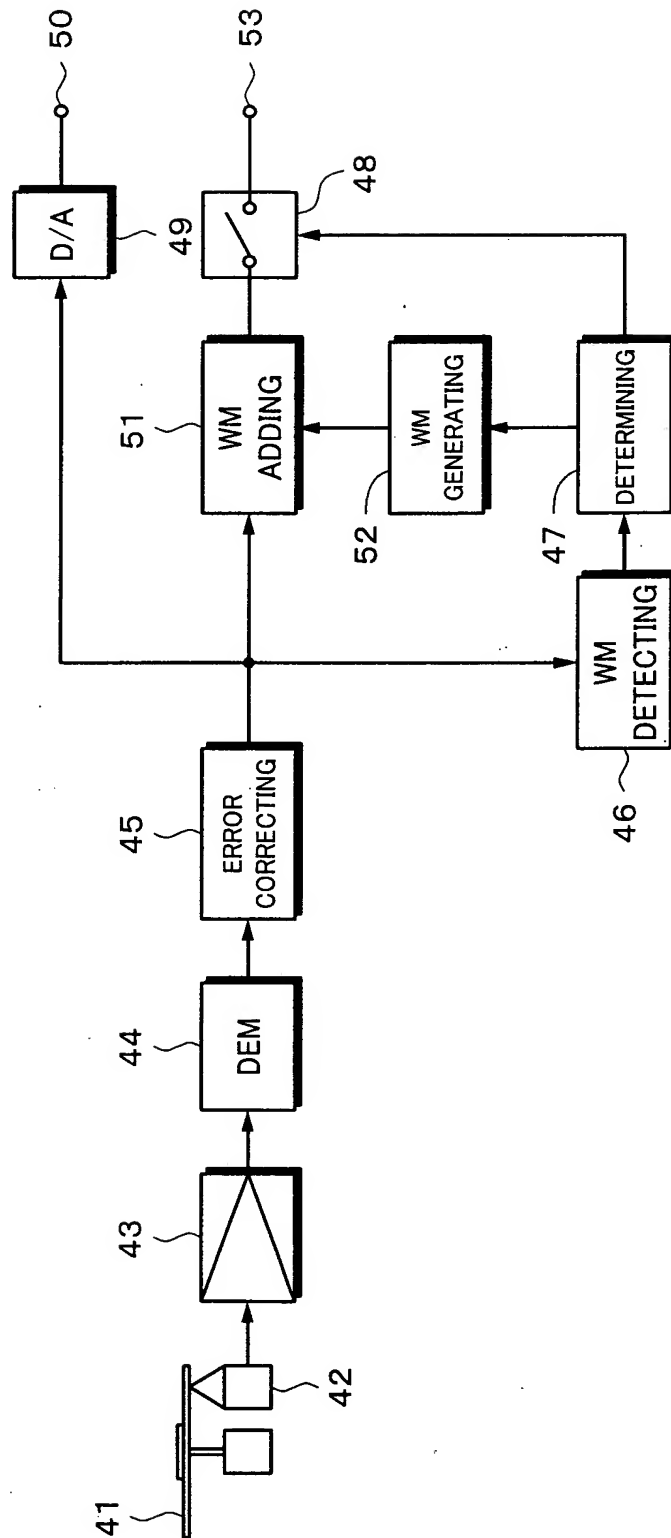


Fig. 11

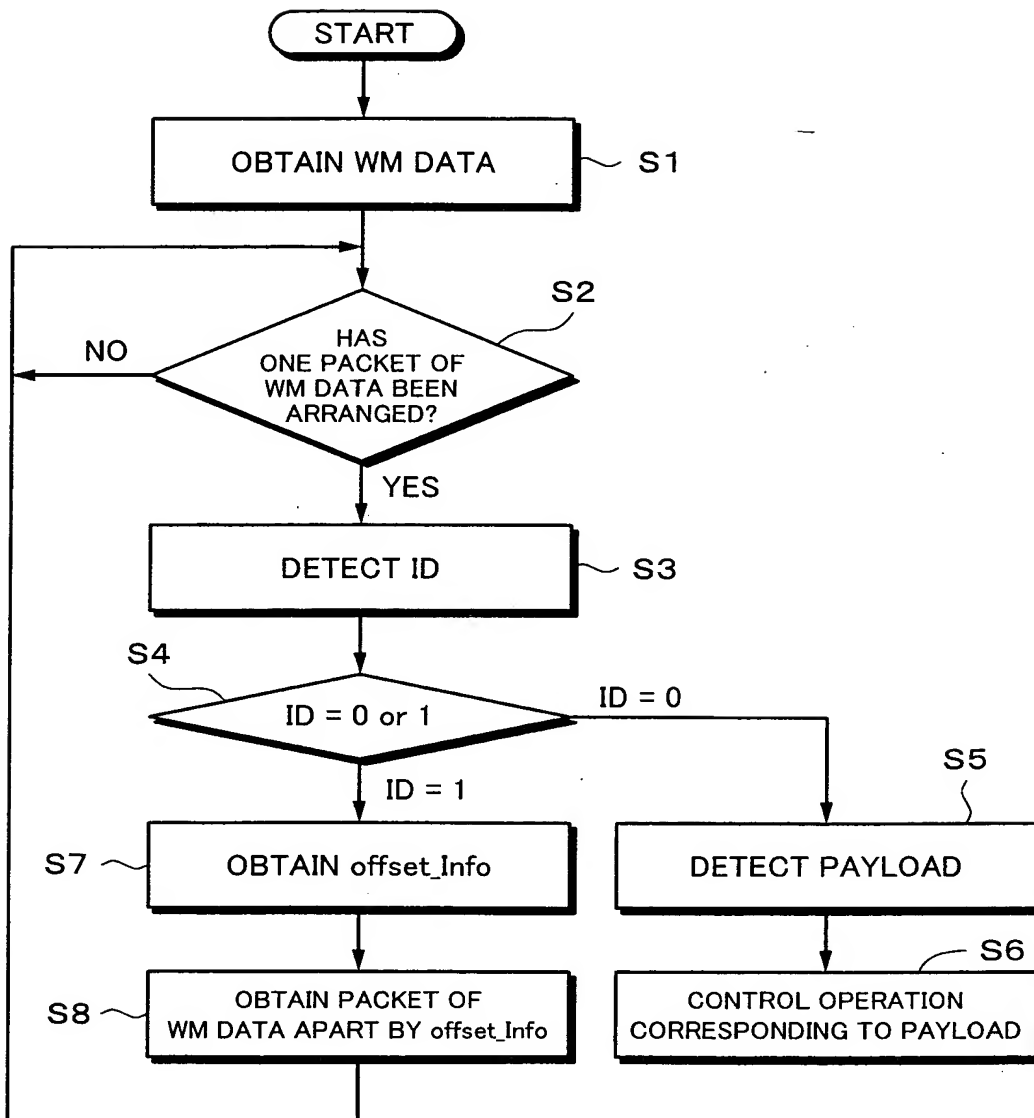


Fig. 12

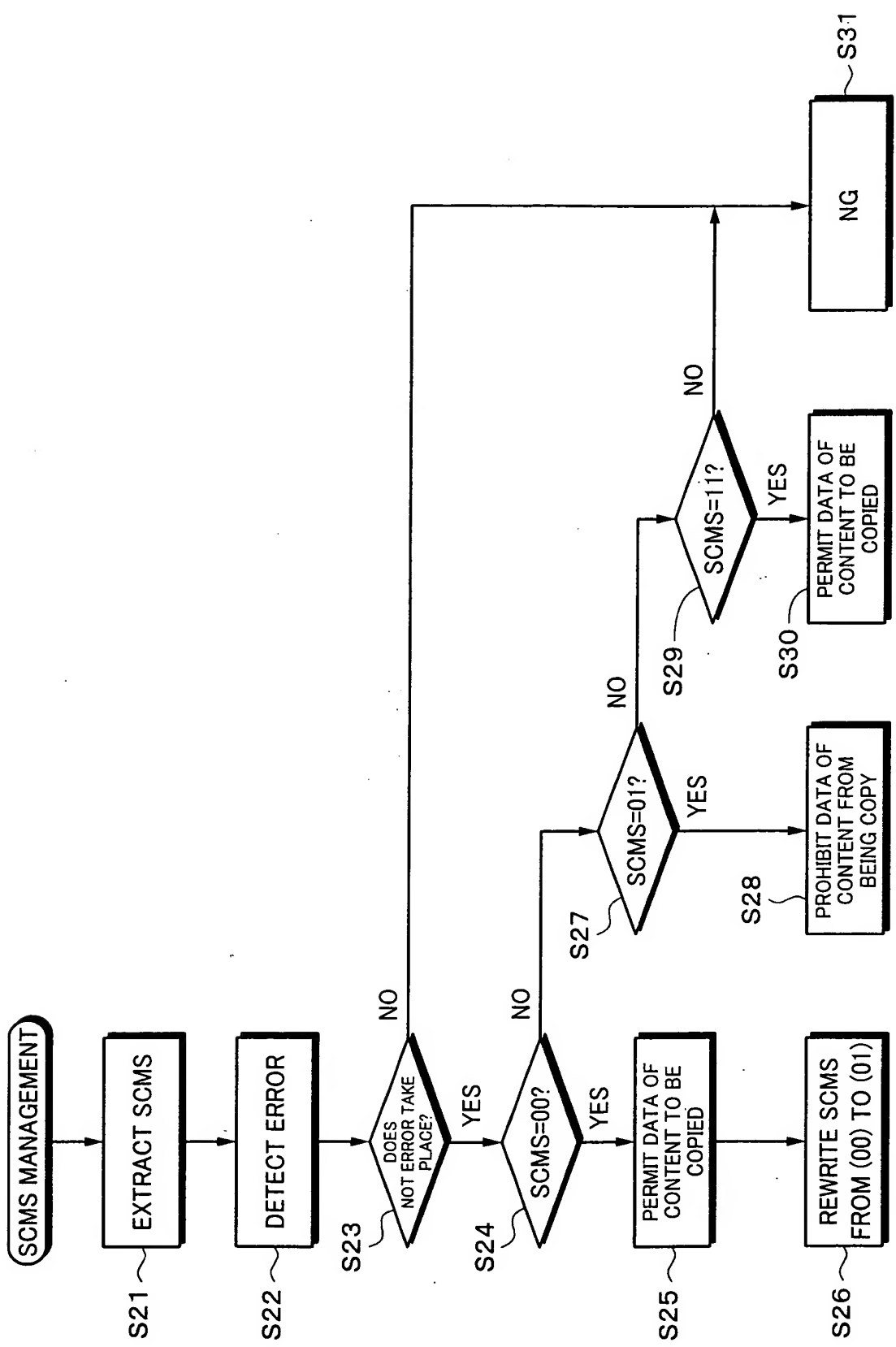
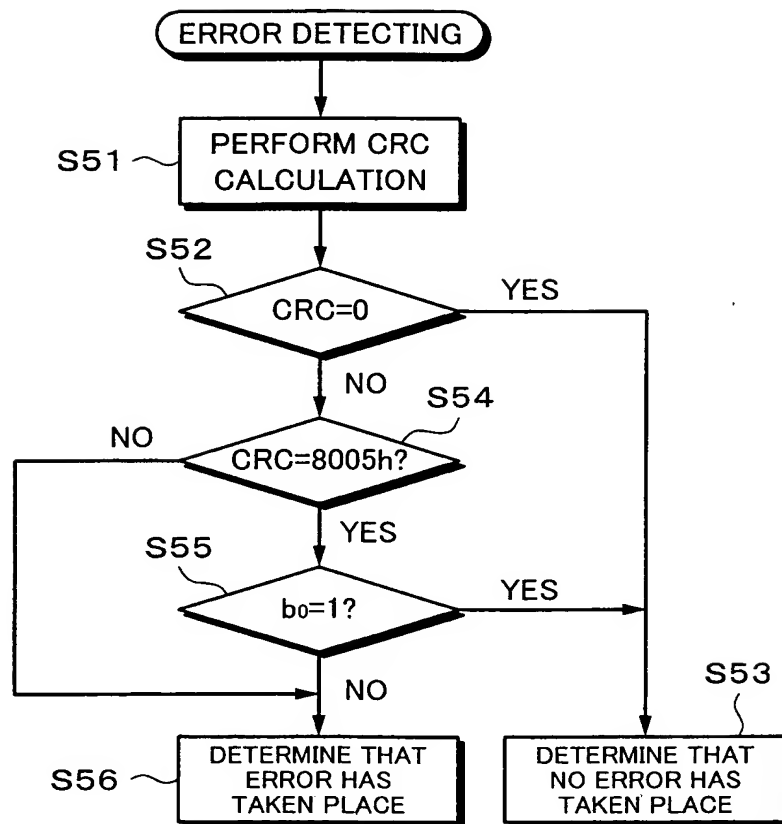


Fig. 13



DESCRIPTION OF REFERENCE NUMERALS

- 2 WATERMARK ADDING CIRCUIT
- 4 WATERMARK GENERATING CIRCUIT
- 5 ERROR CORRECTION CODE ENCODING CIRCUIT
- 9, 41 OPTICAL DISC
- 13 RANDOMIZER
- 45 ERROR CORRECTING CIRCUIT
- 46 WATERMARK DETECTING CIRCUIT
- 47 DETERMINING CIRCUIT
- 48 SWITCH CIRCUIT
- 49 D/A CONVERTER
- 51 WATERMARK ADDING CIRCUIT
- 52 WATERMARK GENERATING CIRCUIT